

1c978 U.S. PTO  
09/784244  
02/14/01

	Subclass
Class	
ISSUE CLASSIFICATION	

PATENT NUMBER

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## U.S. UTILITY Patent Application

O.I.P.E. SCANNED	PATENT DATE AA(4) Q.A. 1C
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APPLICATION NO. 09/784244	CONT/PRIOR D	CLASS 710	SUBCLASS 305	ART UNIT 2181	EXAMINER AURE
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APPLICANTS

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TITLE

Response and data phases in a highly pipelined bus architecture

PTO-2040  
12/99

### ISSUING CLASSIFICATION

ORIGINAL		CROSS REFERENCE(S)			
CLASS	SUBCLASS	CLASS	SUBCLASS (ONE SUBCLASS PER BLOCK)		
<b>INTERNATIONAL CLASSIFICATION</b>					
<input type="checkbox"/> Continued on Issue Slip Inside File Jacket					

<input type="checkbox"/> TERMINAL DISCLAIMER	DRAWINGS			CLAIMS ALLOWED	
	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims	Print Claim for O.G.
<input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed.	(Assistant Examiner) _____ (Date) _____			<b>NOTICE OF ALLOWANCE MAILED</b>	
<input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S Patent. No. _____	(Primary Examiner) _____ (Date) _____			<b>ISSUE FEE</b>	
<input type="checkbox"/> The terminal _____ months of this patent have been disclaimed.	(Legal Instruments Examiner) _____ (Date) _____			<b>ISSUE BATCH NUMBER</b>	

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